**DIGITAL LOGICS AND ARCHITECTURE LAB**

**EXPERIMENT 2-REALIZING BASIC GATES**

**1. OR GATE**

A logical operation which gives the value one if at least one operand has the value one, and otherwise gives a value of zero.

Calendar

Description automatically generated

Calendar

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**2.AND GATE**

A Boolean operator which gives the value one if and only if all the operands are one, and otherwise has a value of zero.

Table, calendar

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Table, calendar

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**3.NOT GATE**

In digital logic, an inverter or NOT gate is a [logic gate](https://en.wikipedia.org/wiki/Logic_gate) which implements [logical negation](https://en.wikipedia.org/wiki/Logical_negation). In [mathematical logic](https://en.wikipedia.org/wiki/Mathematical_logic) it is equivalent to the logical negation operator.

Calendar

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Calendar

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